

S/N Unknown

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Richard W. Ollila et al.	Examiner:	Unknown
Serial No.:	Unknown	Group Art Unit:	Unknown
Filed:	Herewith	Docket:	884.295US2
Title:	TEST UNIT AND ENCLOSURE FOR TESTING INTEGRATED CIRCUITS		
Assignee:	Intel Corporation	Customer No.:	21186

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement.

Pursuant to 37 C.F.R. §1.98(d), copies of the listed documents are not provided as these references were previously cited by or submitted to the U.S. Patent Office in connection with Applicants' prior U.S. application, Serial No. 09/706,952, filed on November 6, 2000, which is relied upon for an earlier filing date under 35 U.S.C. §120.

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Serial No : Unknown

Filing Date: Herewith

Title: TEST UNIT AND ENCLOSURE FOR TESTING INTEGRATED CIRCUITS

Assignee: Intel Corporation

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The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

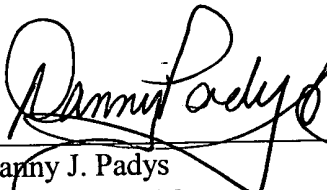
Respectfully submitted,

RICHARD W. OLLILA ET AL.

By their Representatives,

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Date September 8, 2003

By 
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Date of Deposit: September 8, 2003

This paper or fee is being deposited on the date indicated above with the United States Postal Service pursuant to 37 CFR 1.10, and is addressed to the Commissioner for Patents, Mail Stop Patent Application, P.O. Box 1450, Alexandria, VA 22313-1450.

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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)	<i>Complete if Known</i>	
	Application Number	Unknown
	Filing Date	Even Date Herewith
	First Named Inventor	Ollila, Richard
	Group Art Unit	Unknown
	Examiner Name	Unknown
Sheet 1 of 1	Attorney Docket No: 884.295US2	

US PATENT DOCUMENTS						
Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
	US-4,326,165	04/20/1982	Szedon, John R.	324	158	01/10/1980
	US-5,065,089	11/12/1991	Rich, Donald S.	324	158	06/01/1990
	US-5,220,277	06/15/1993	Reitingner, Erich	324	158	03/25/1992
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	US-5,949,682	09/07/1999	Dickinson, Gerard T., et al.	364	468.28	05/28/1998
	US-6,114,868	09/05/2000	Nevill, Leland R.	324	760	09/02/1999

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²

EXAMINER

DATE CONSIDERED